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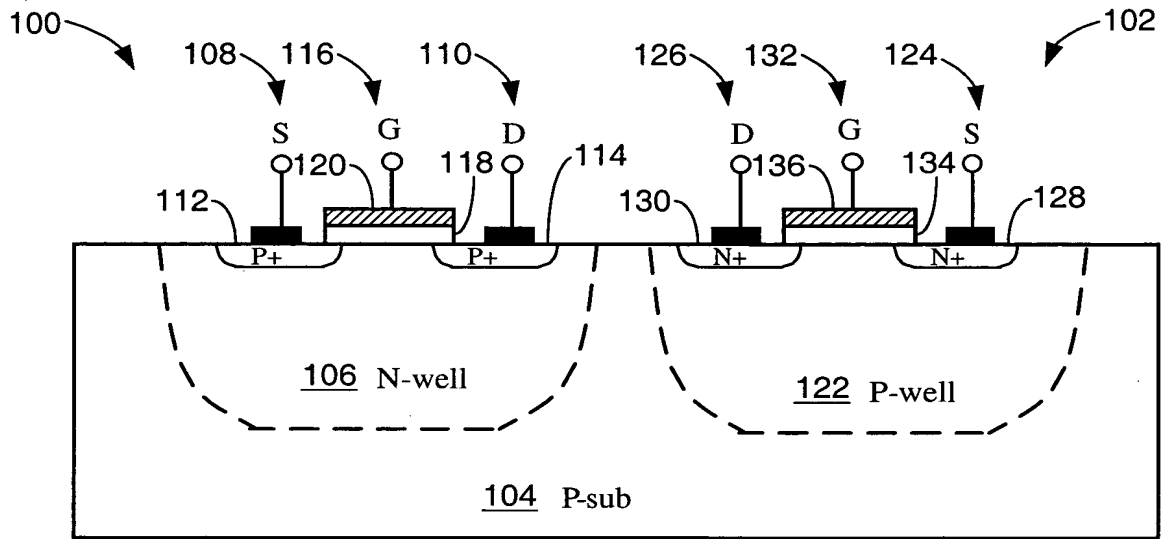


FIG._1 (PRIOR ART)

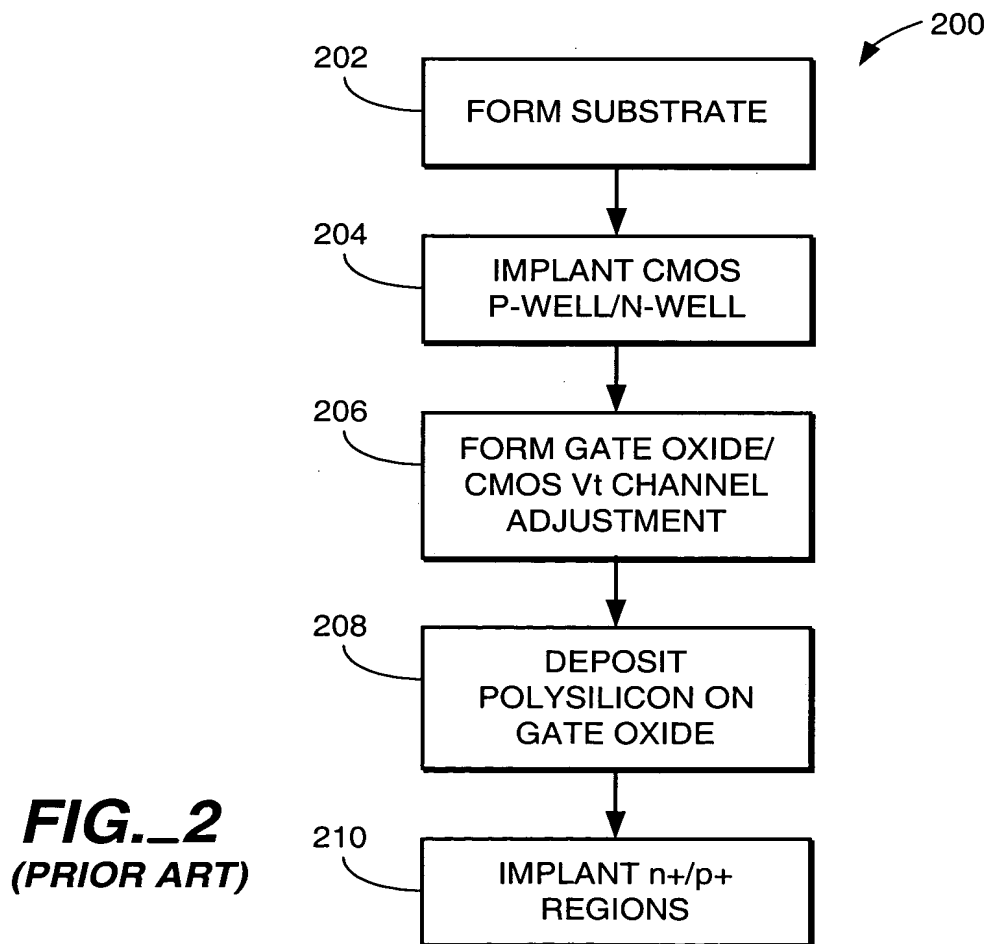
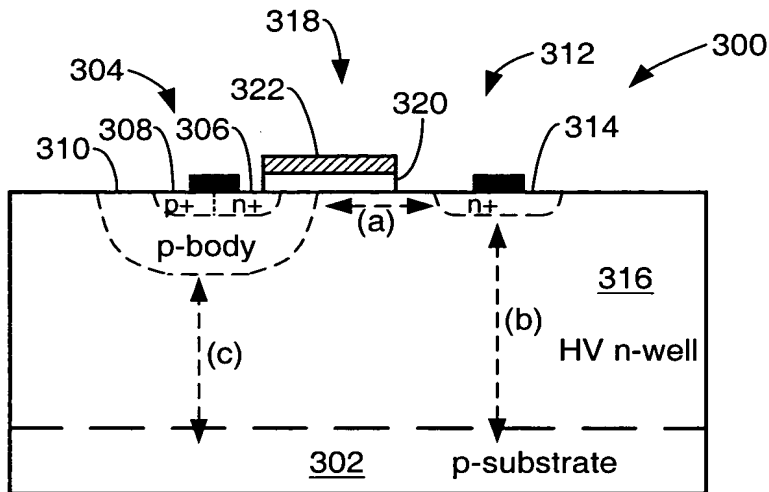
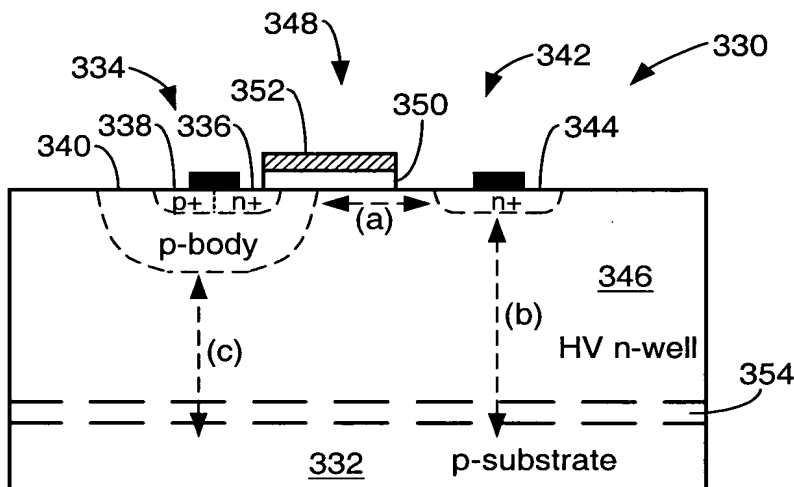


FIG._2
(PRIOR ART)



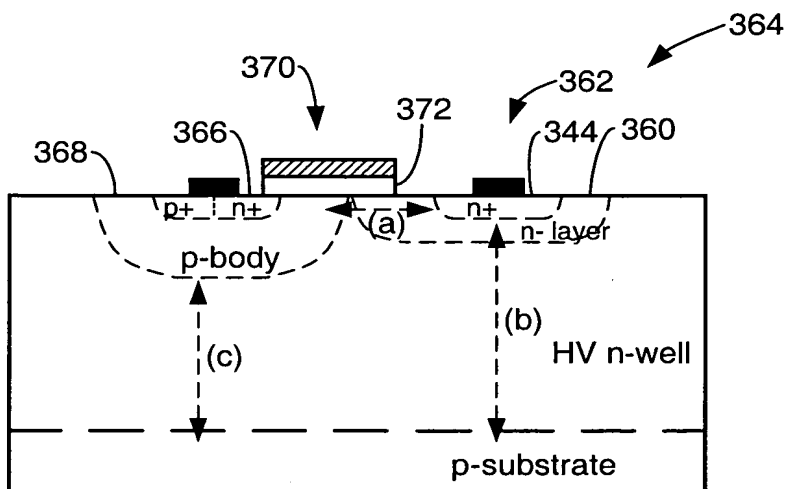
P-Body implanted
after gate formation
disrupts CMOS
thermal budget

FIG. 3A
(PRIOR ART)



P-Body implanted
after gate formation
disrupts CMOS
thermal budget

FIG. 3B
(PRIOR ART)



P-Body implanted
after gate formation
disrupts CMOS
thermal budget

FIG. 3C
(PRIOR ART)

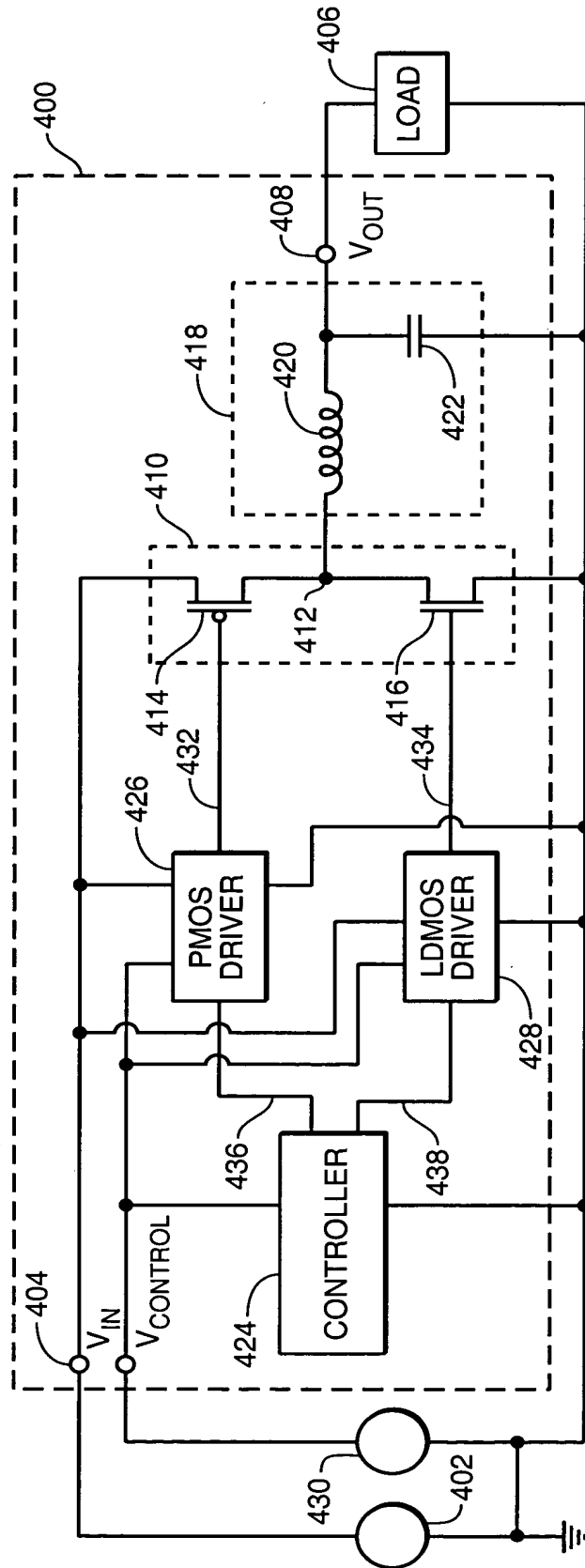


FIG. 4

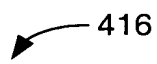


FIG. 5A

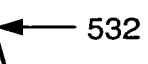


FIG. 5B

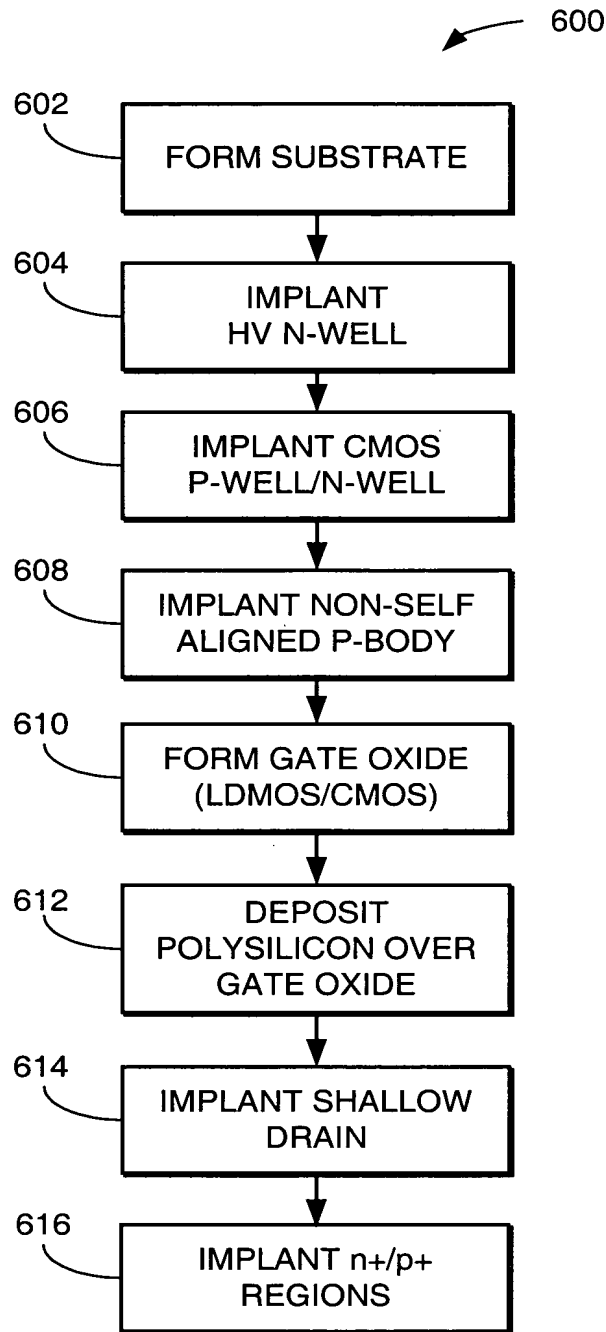


FIG. 6

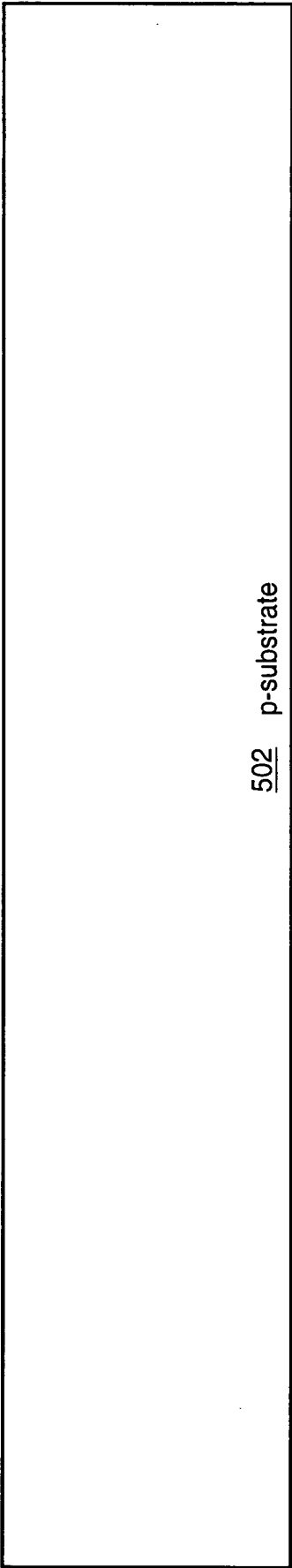


FIG. 7A

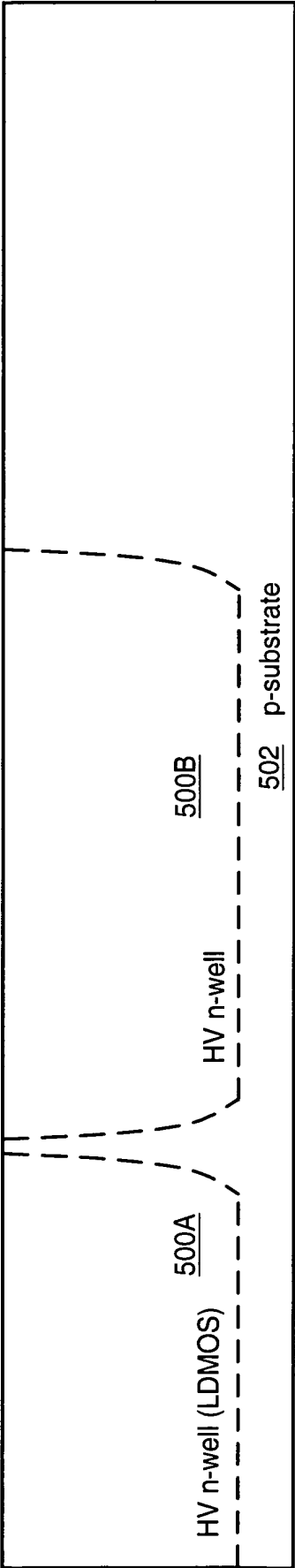


FIG. 7B

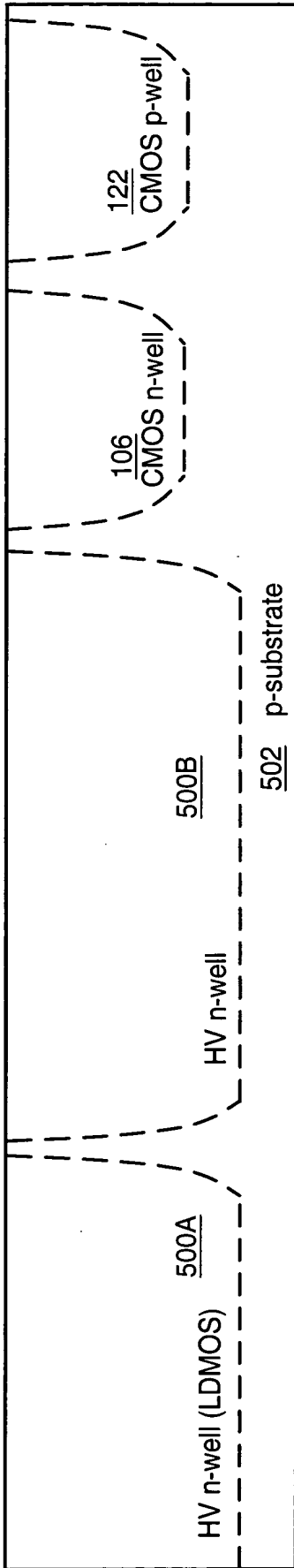


FIG. 7C





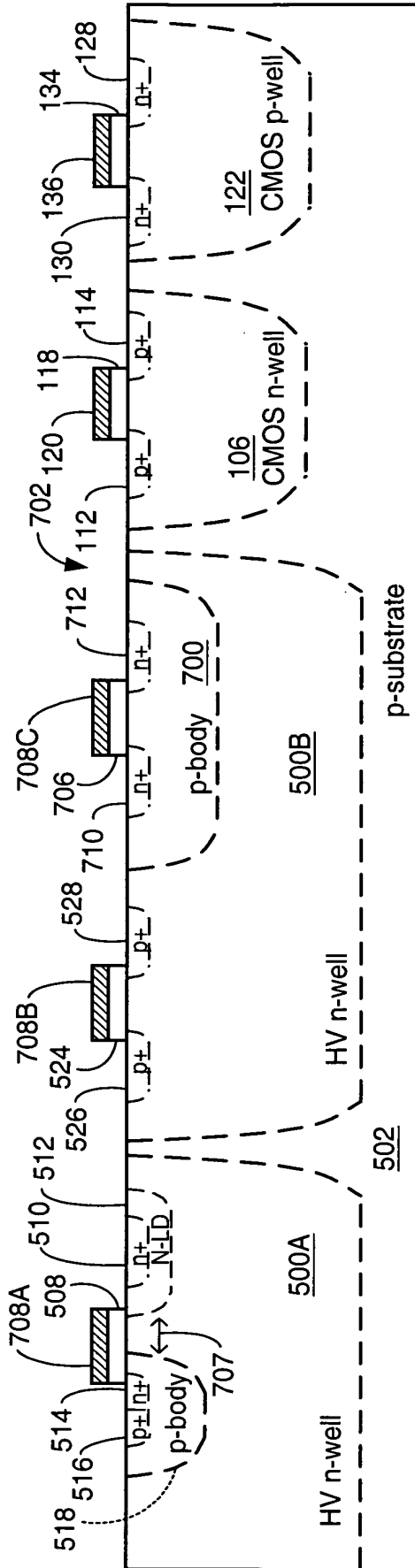


FIG. 7H

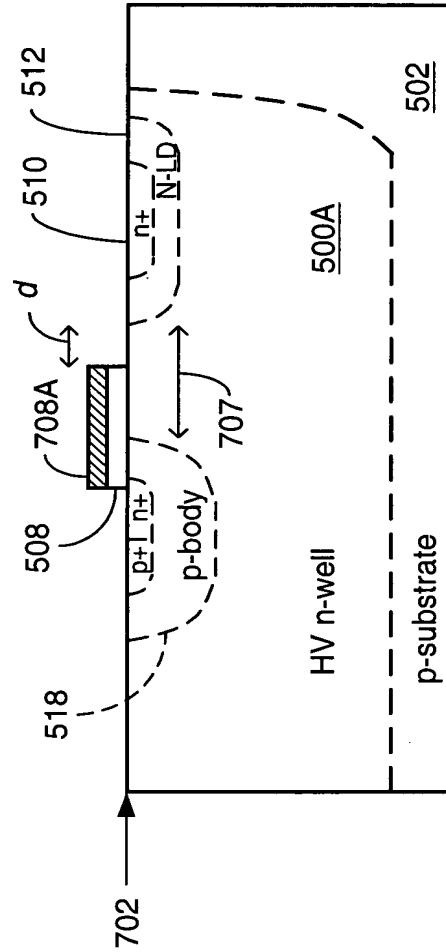


FIG. 9

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FIG._8A

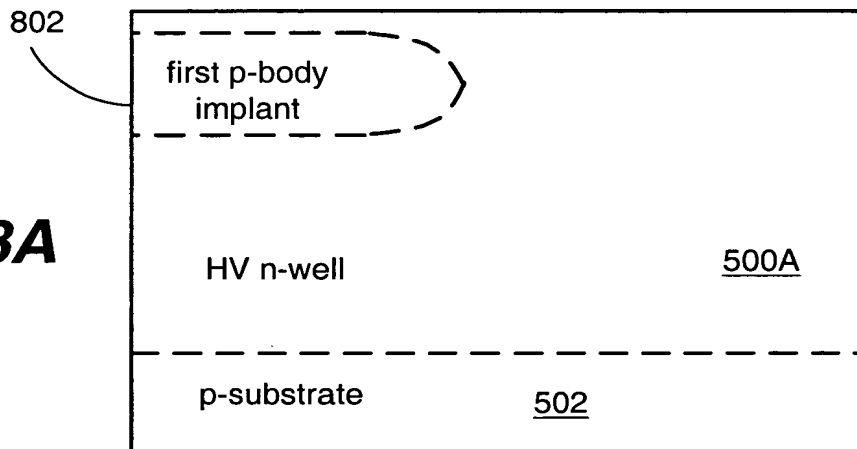


FIG._8B

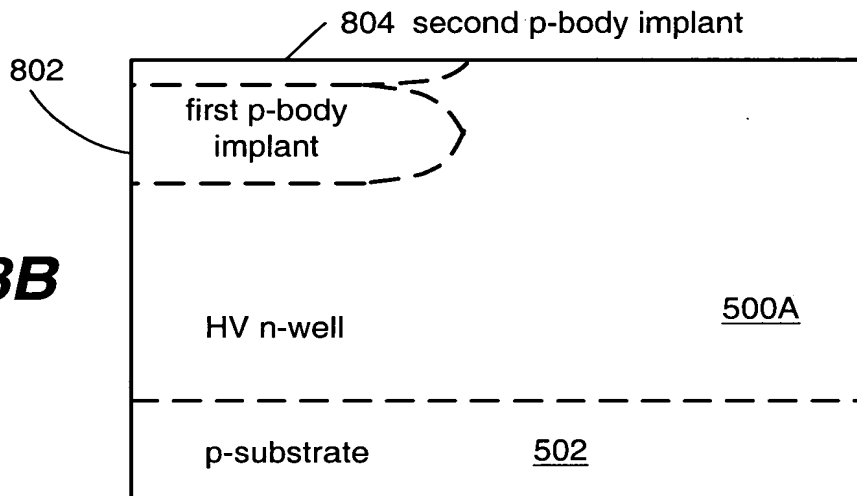
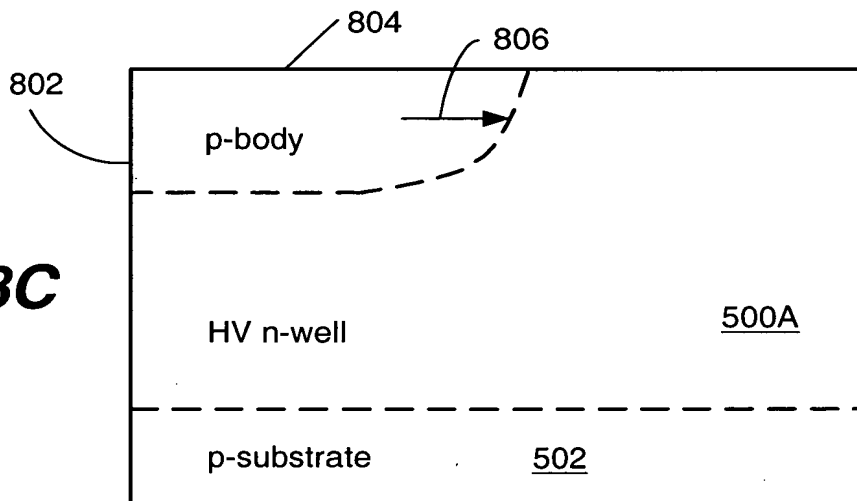


FIG._8C



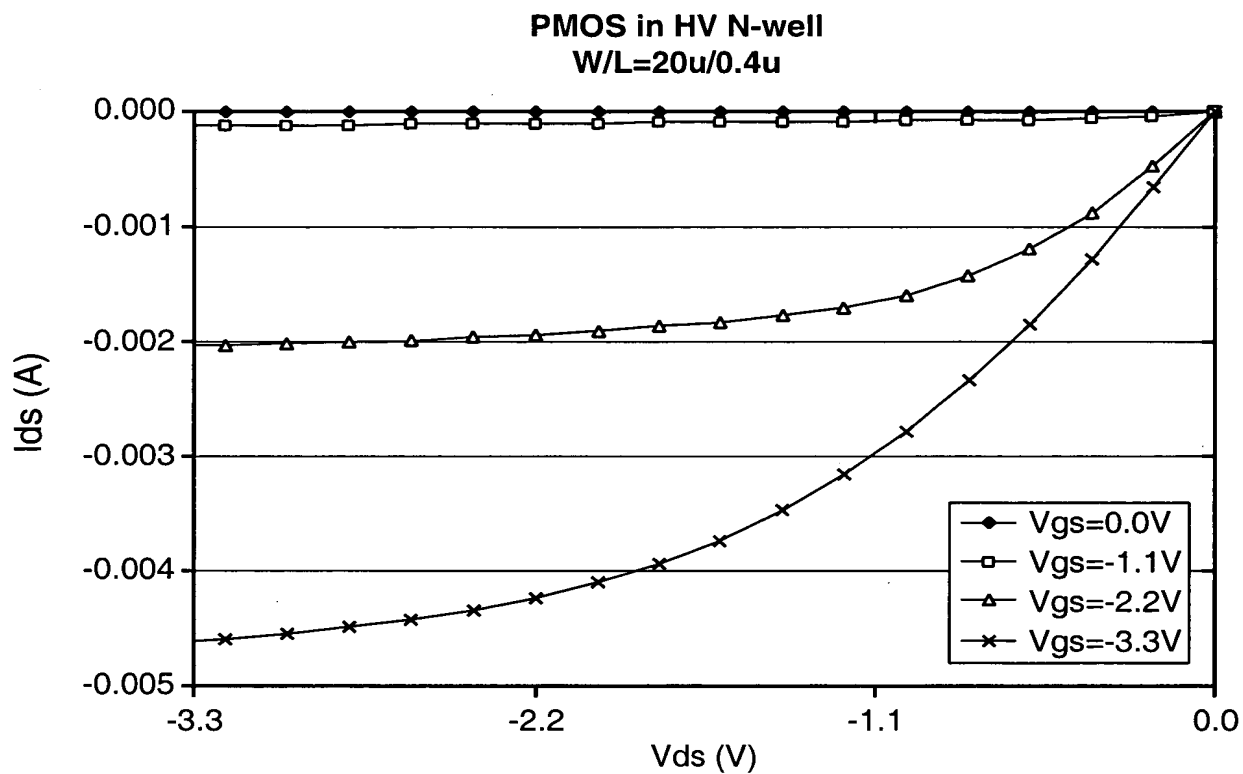


FIG._10A

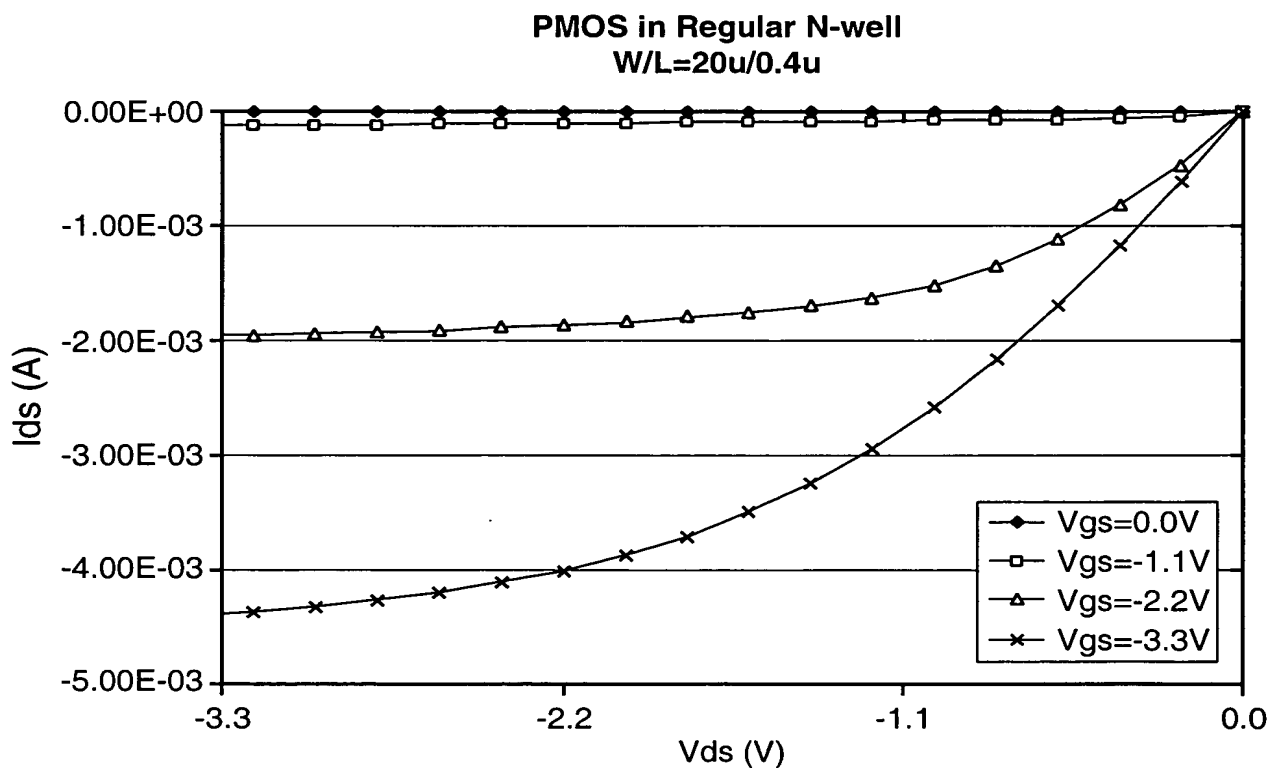


FIG._10B

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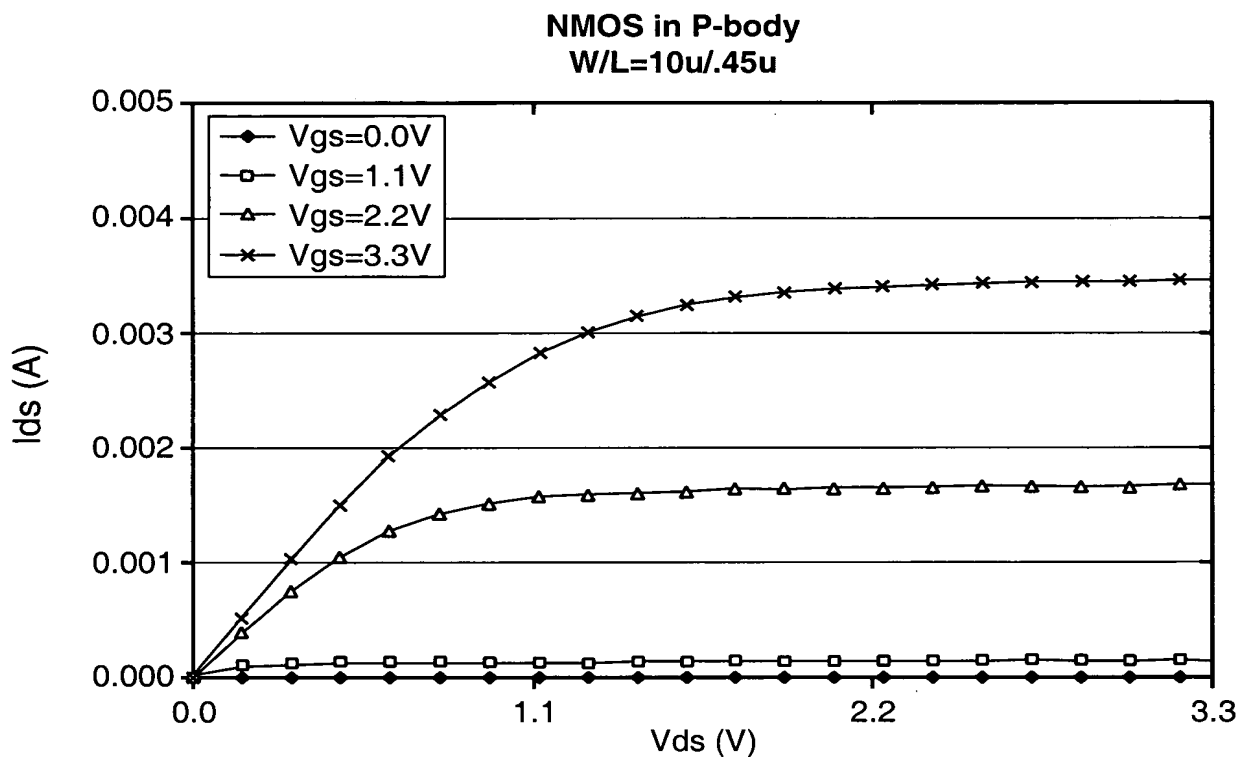


FIG._11A

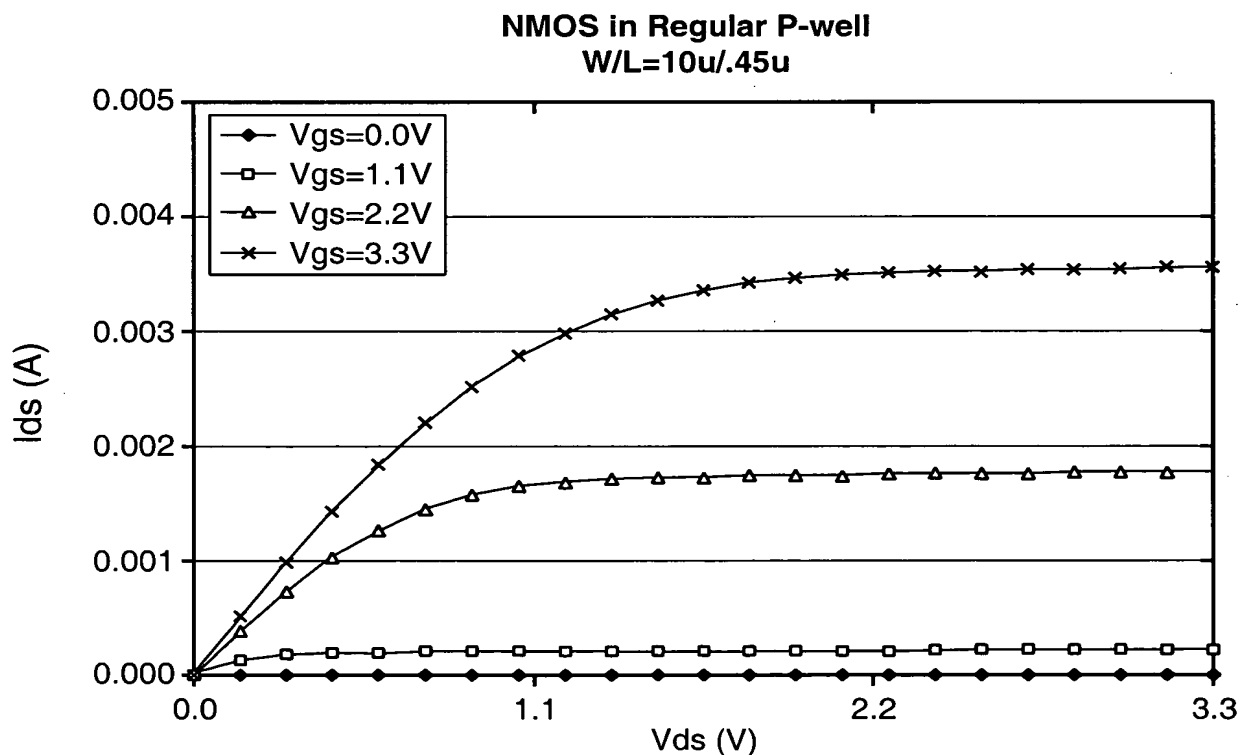


FIG._11B

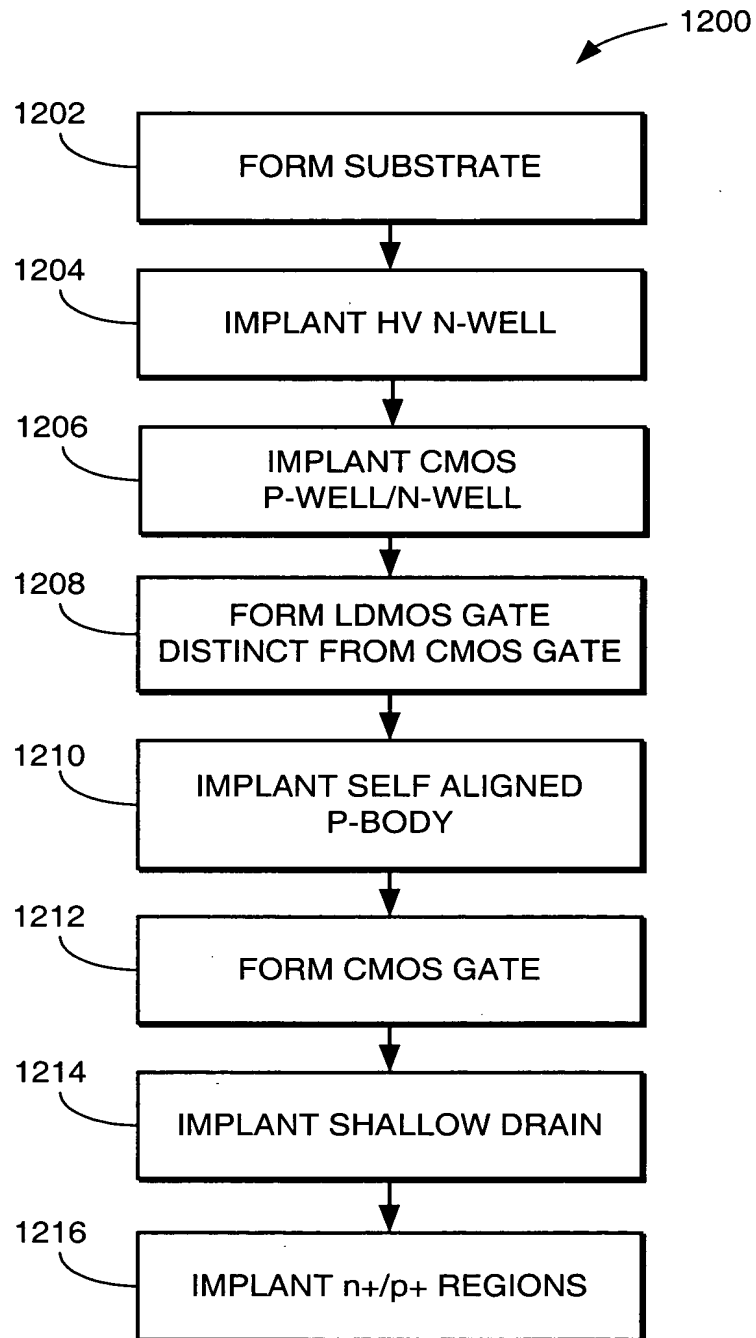


FIG. 12

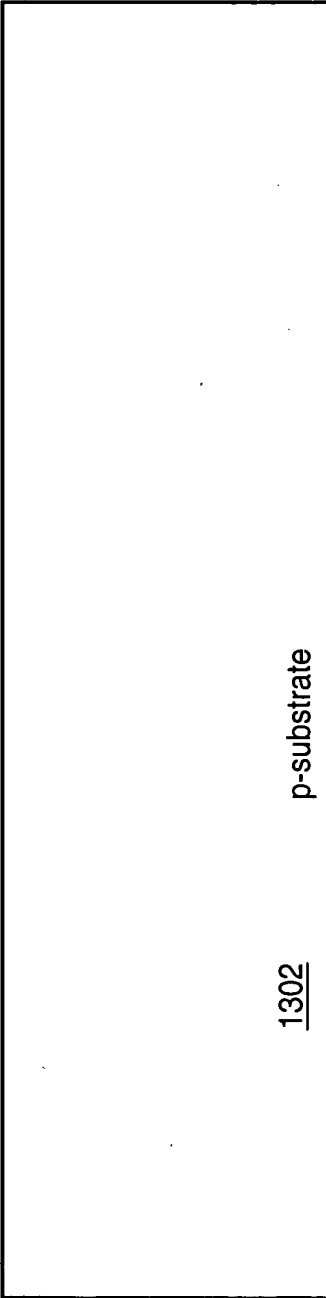


FIG. 13A

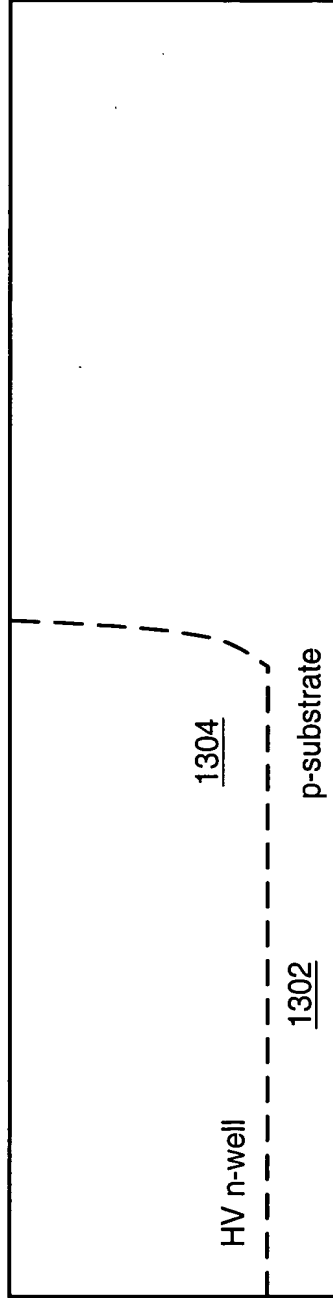


FIG. 13B

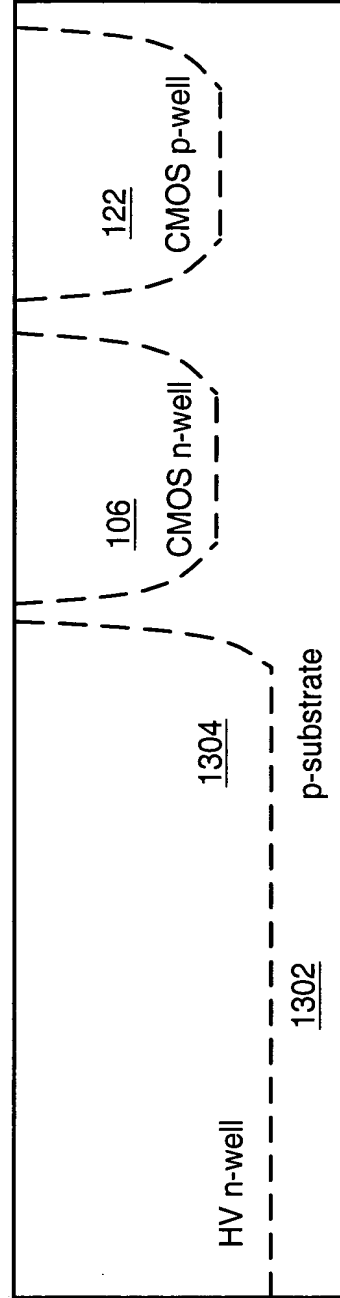


FIG. 13C

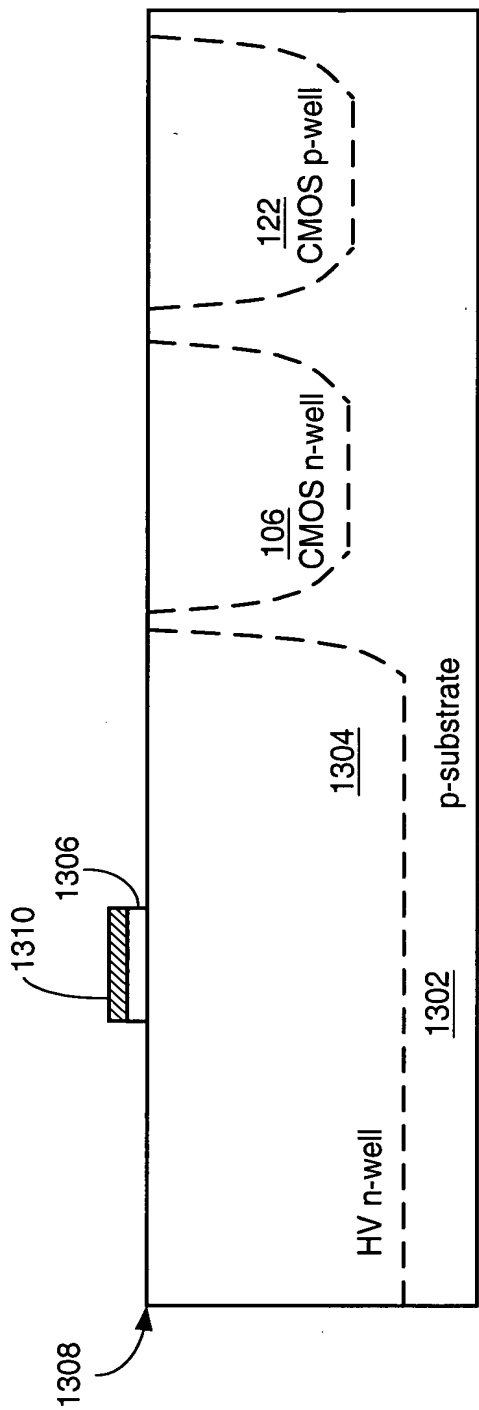


FIG. 13D

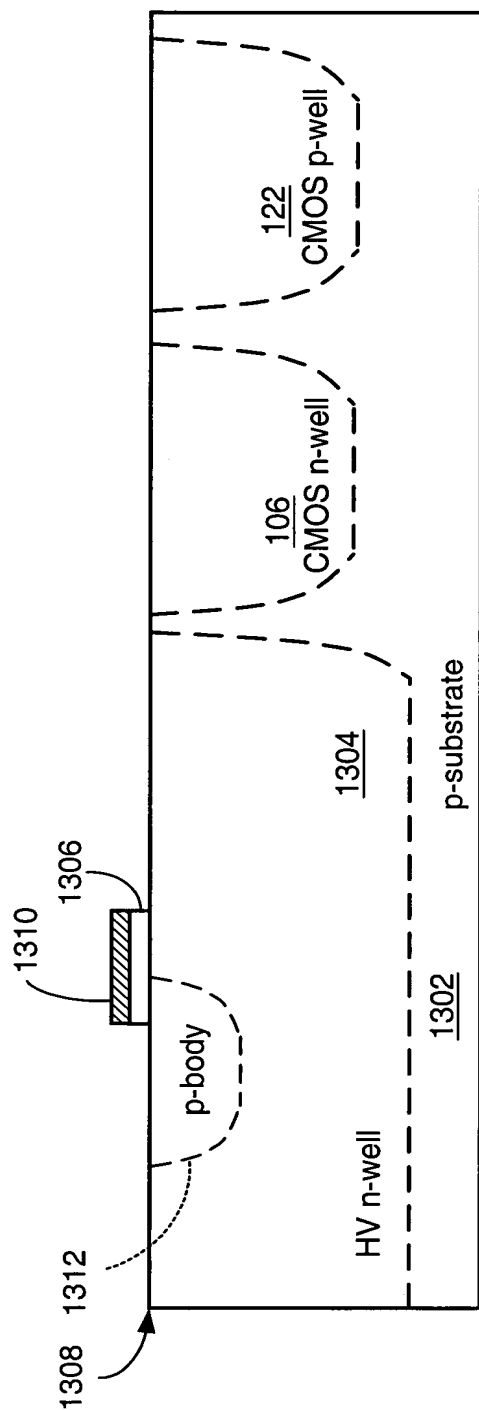


FIG. 13E

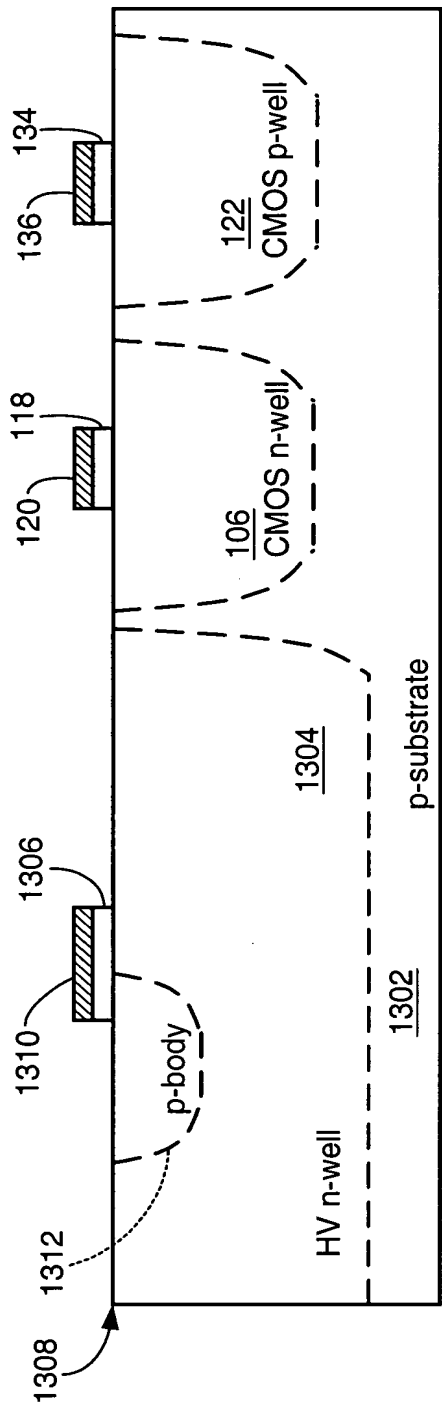


FIG. 13F

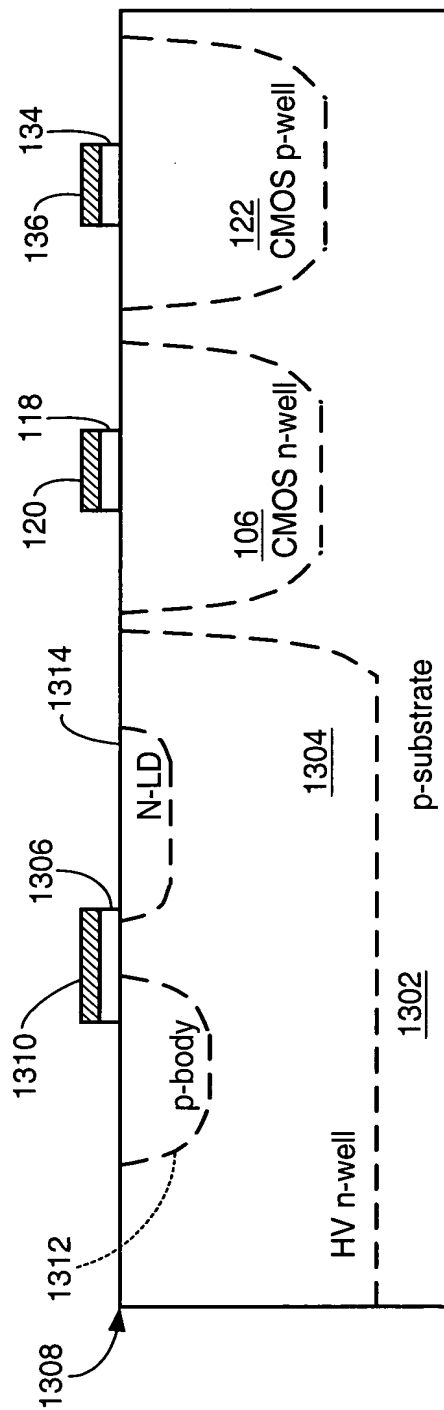


FIG. 13G

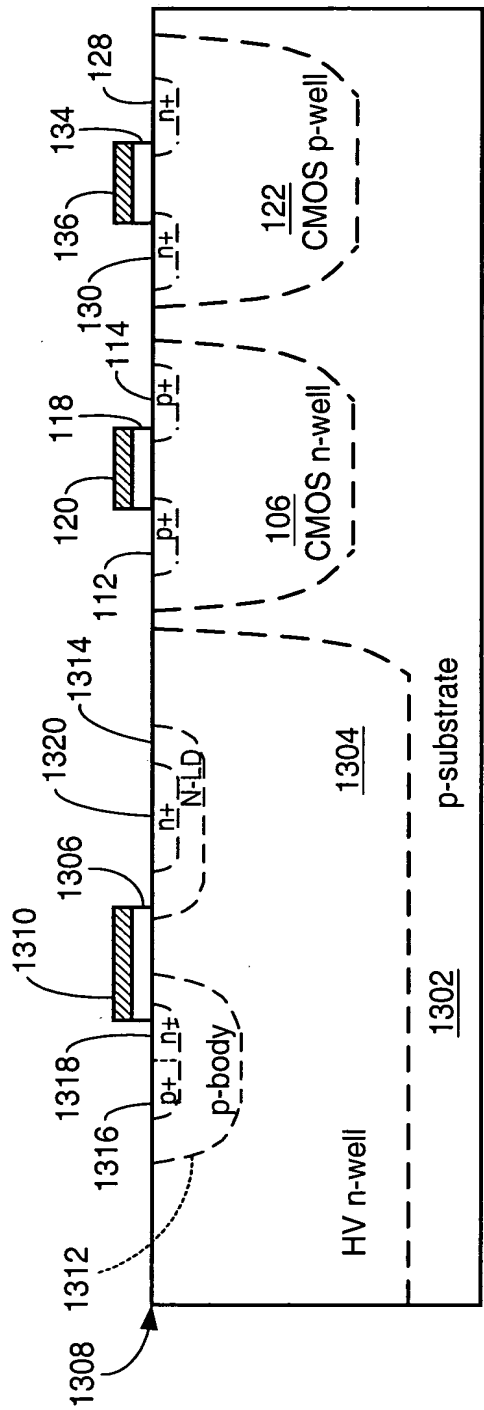


FIG. 13H

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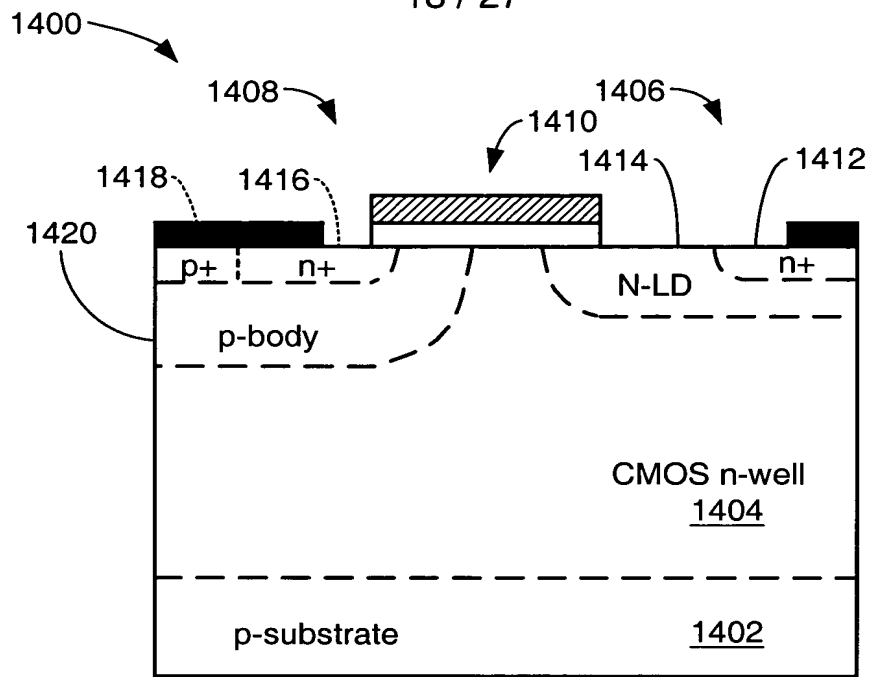


FIG. 14

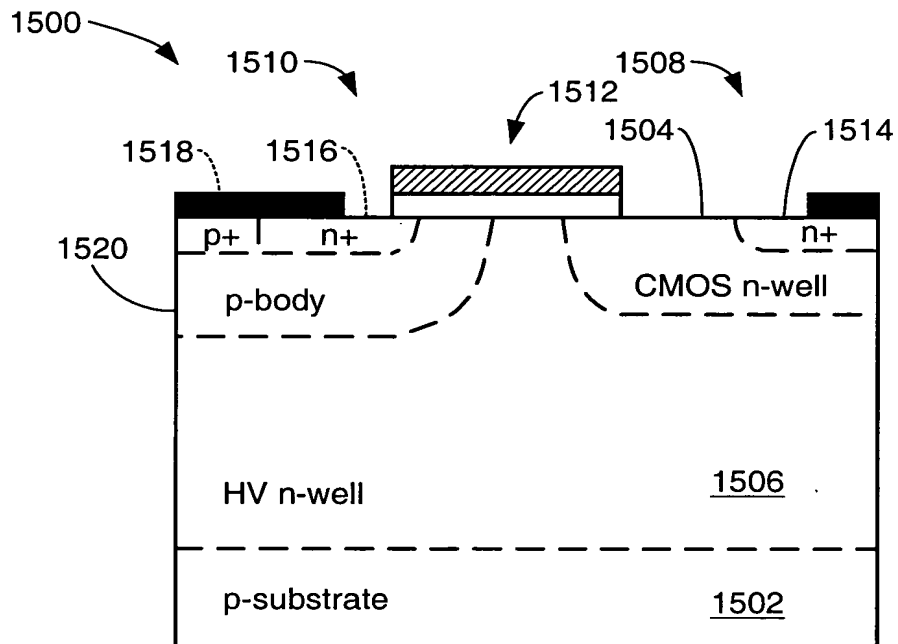


FIG. 15



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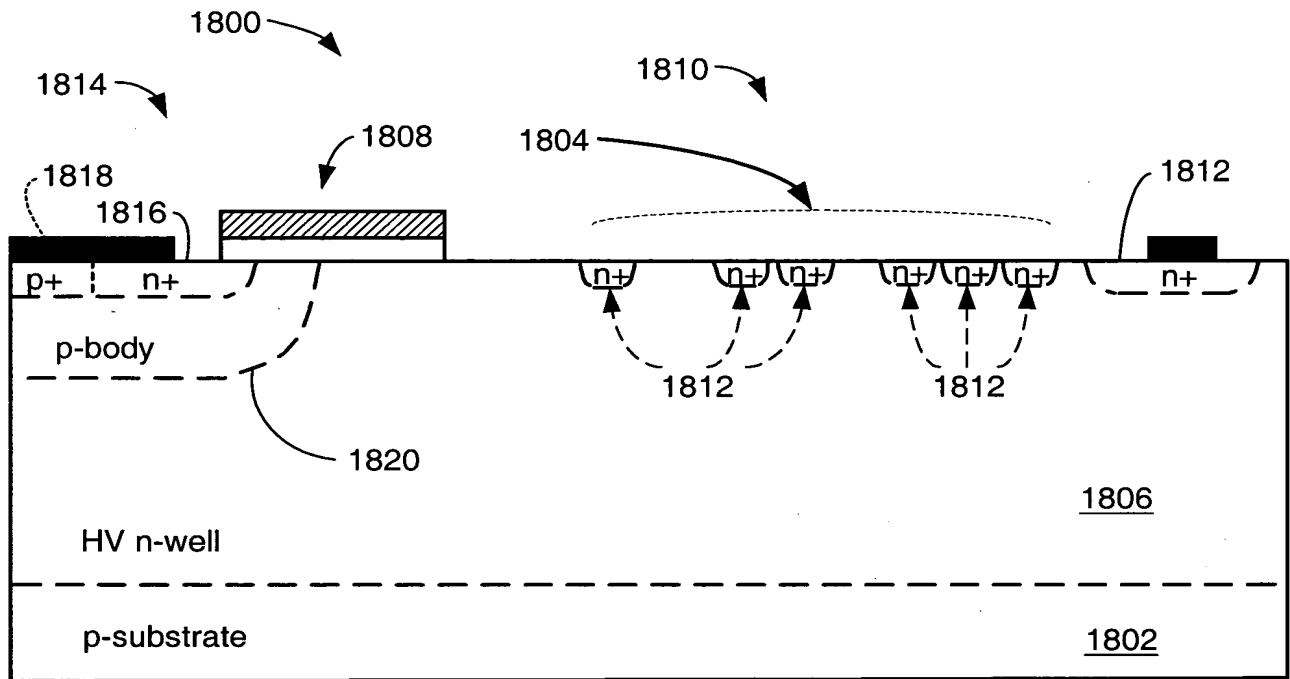


FIG. 18

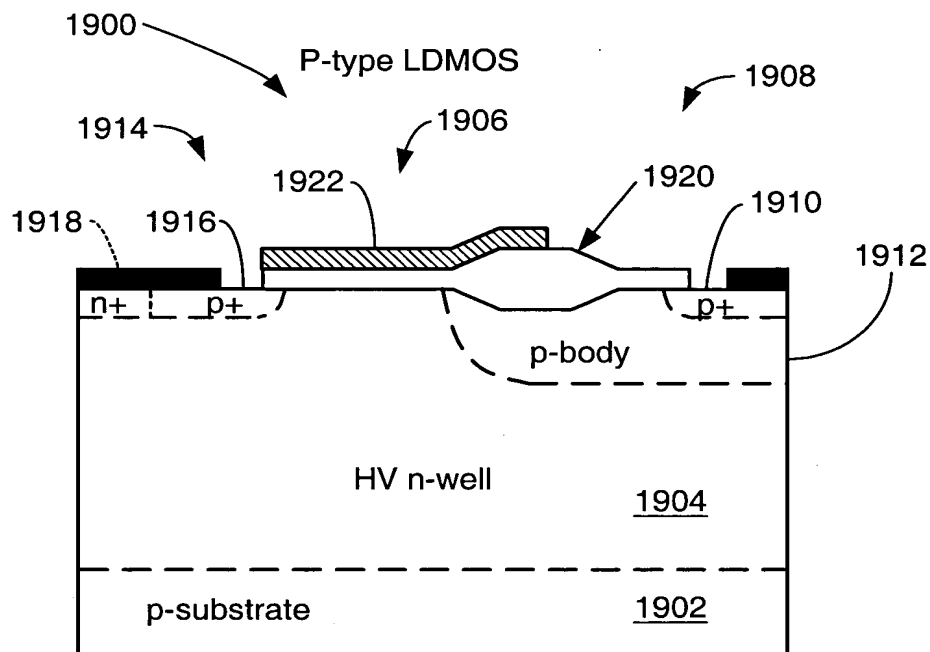


FIG. 19

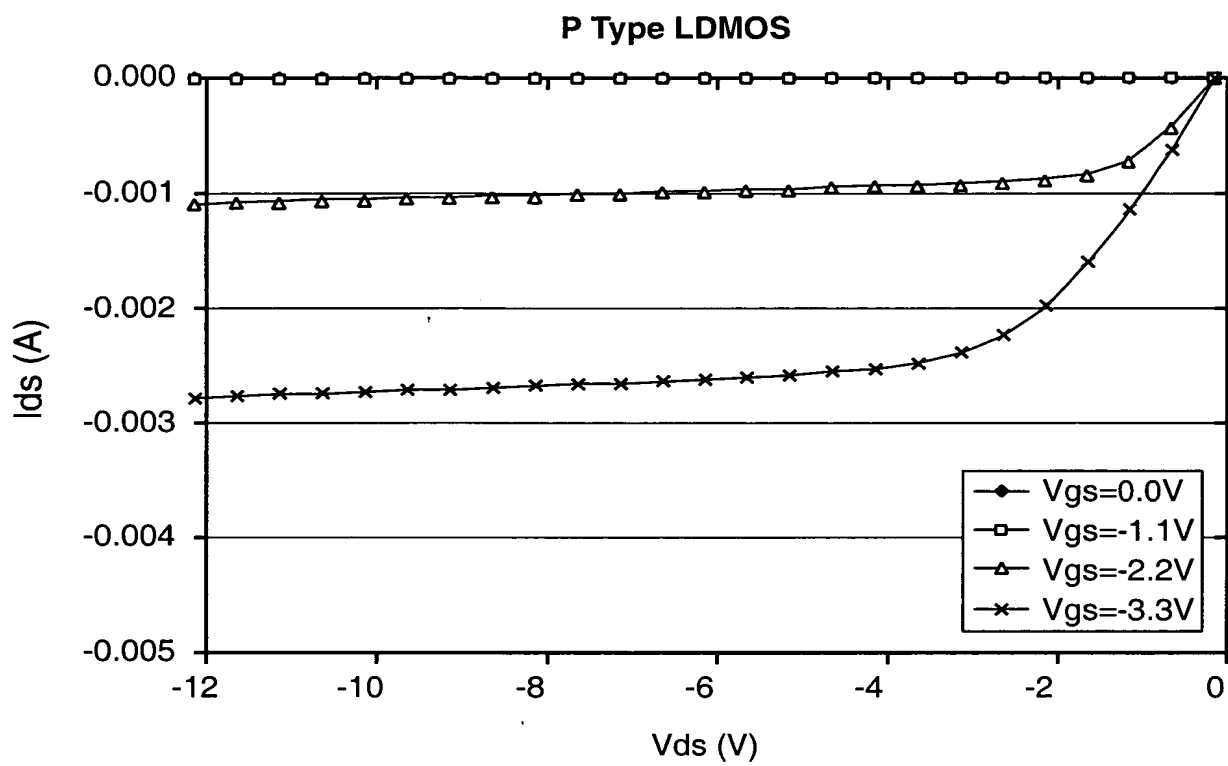


FIG._20

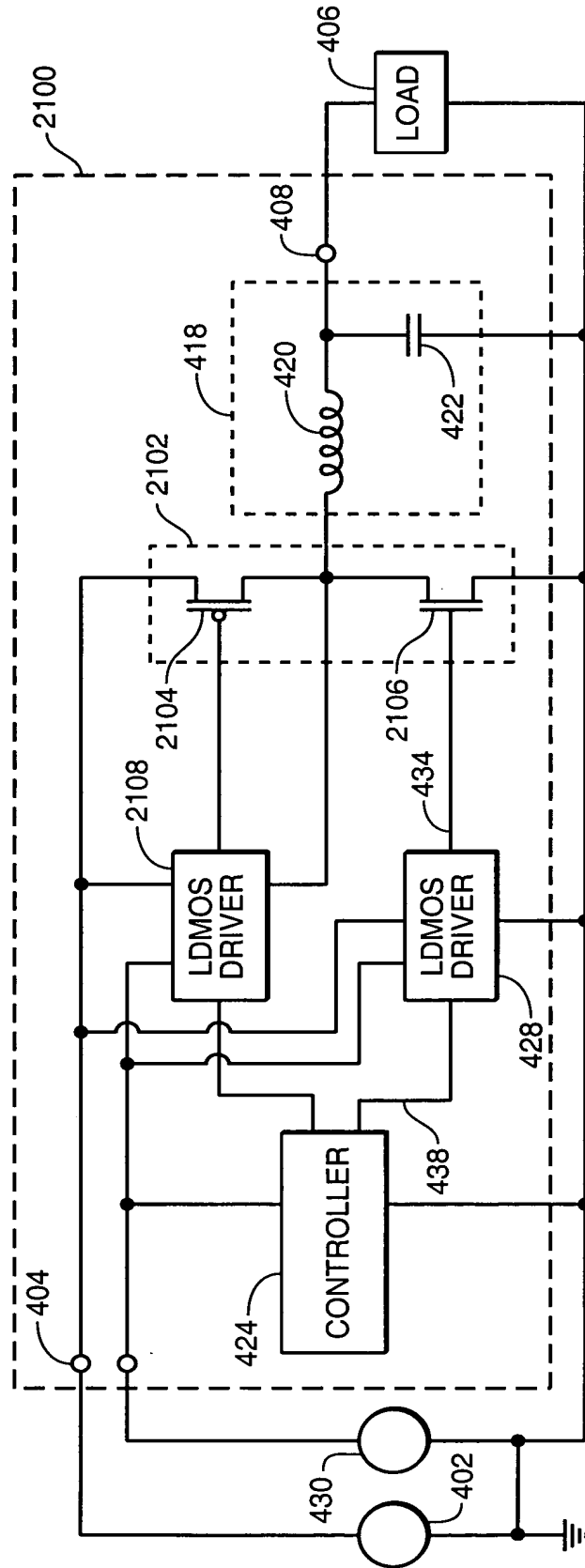


FIG. 21

FIG._22

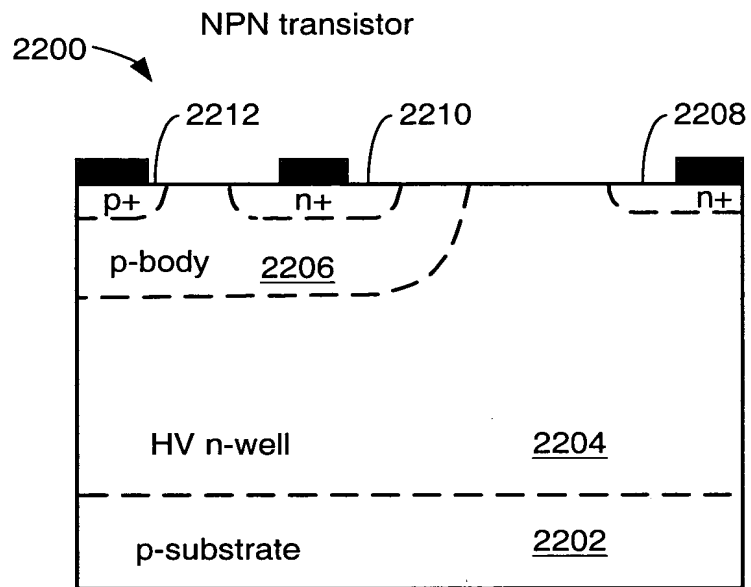
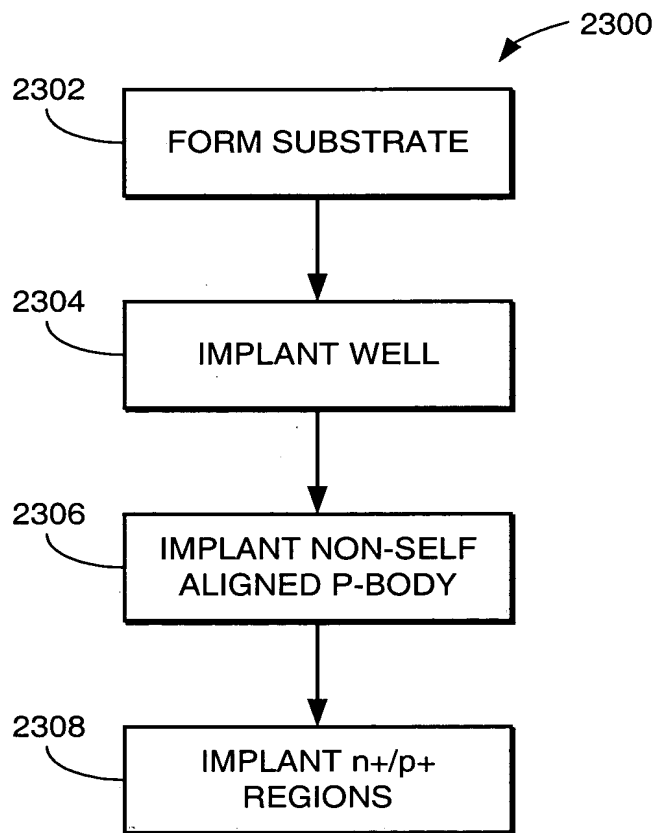


FIG._23



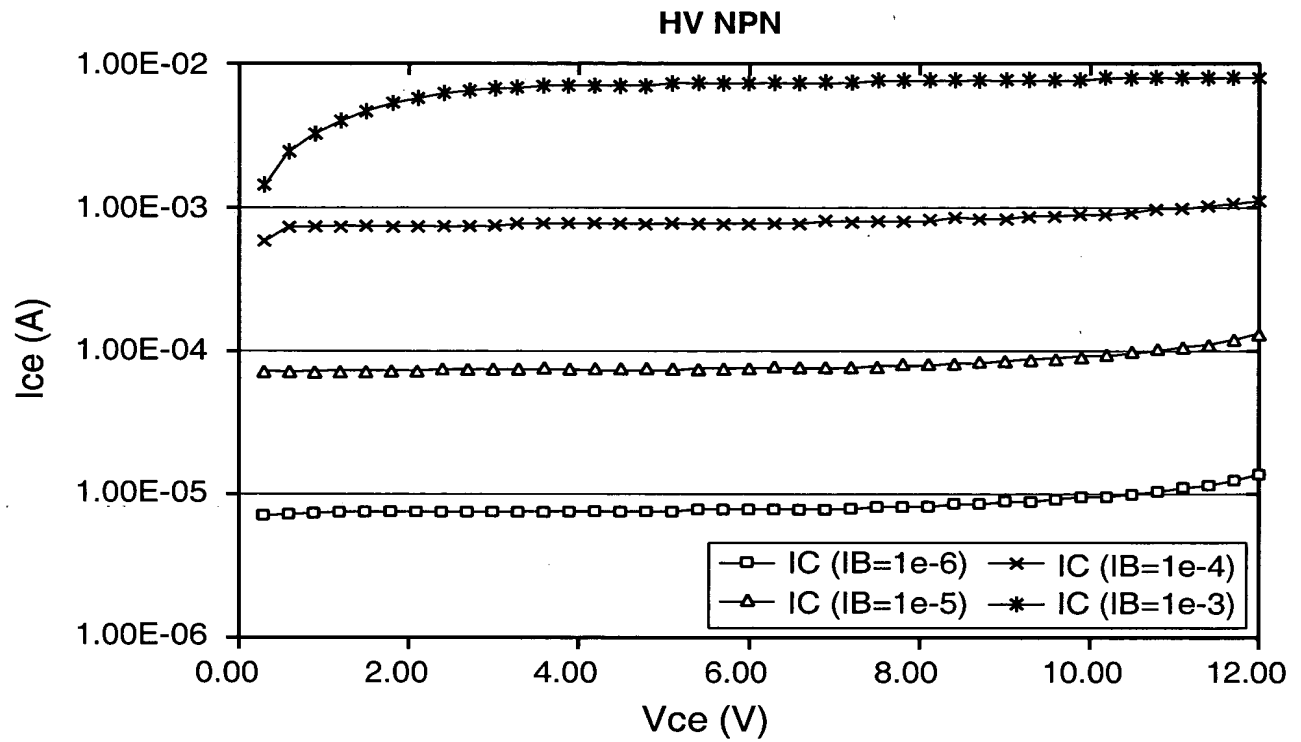


FIG._24

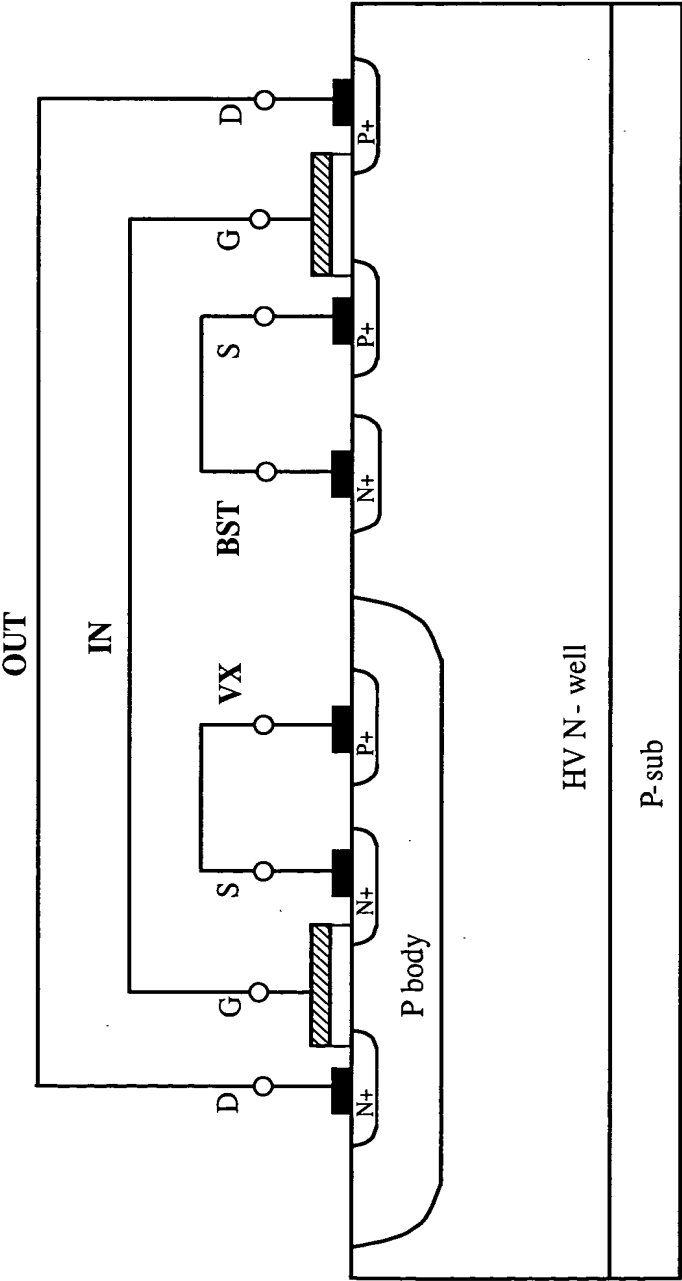


FIG. 25A

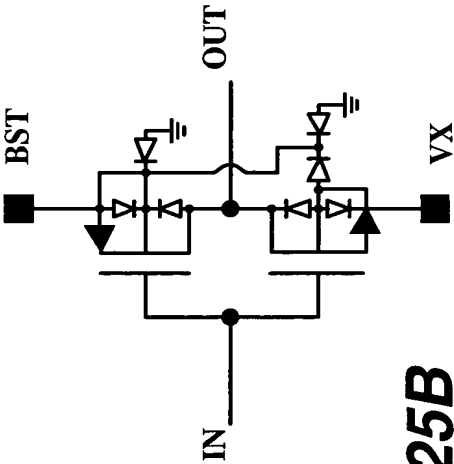


FIG. 25B

**NMOS of Floating CMOS Core ($V_{\text{source}}=12\text{V}$)
($W/L=10\mu/4.5\mu$)**

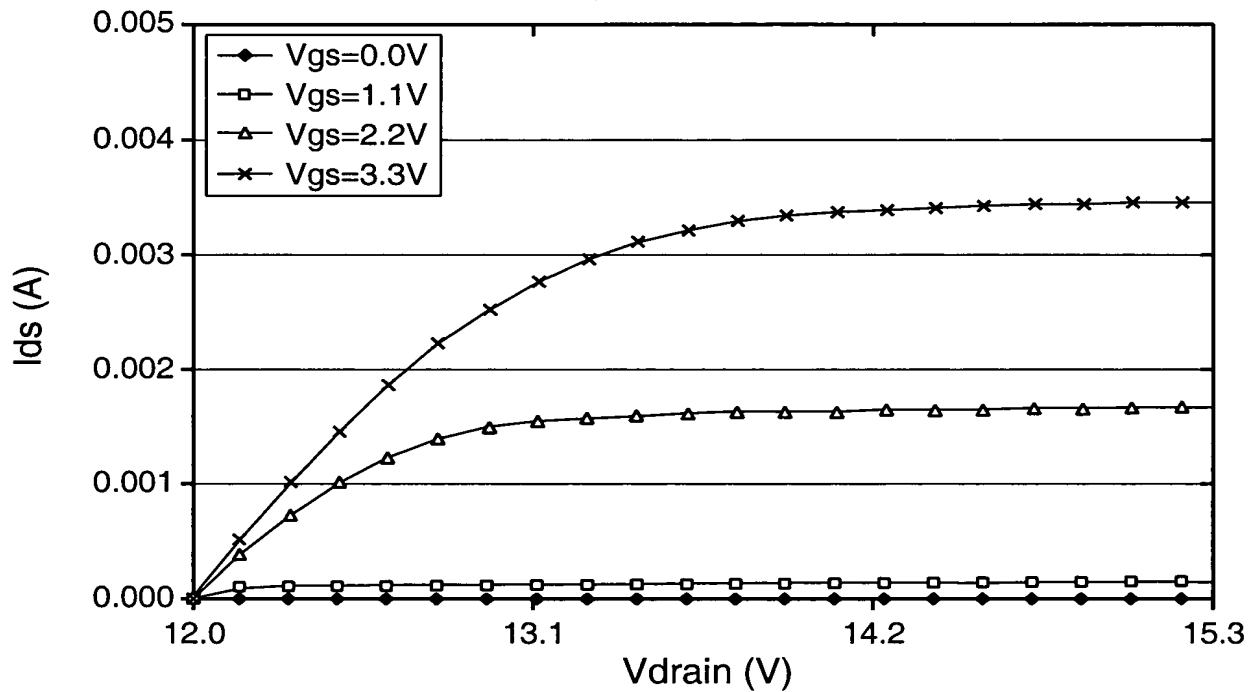


FIG._26A

**PMOS of Floating CMOS Core ($V_{\text{source}}=15.3\text{V}$)
($W/L=20\mu/0.4\mu$)**

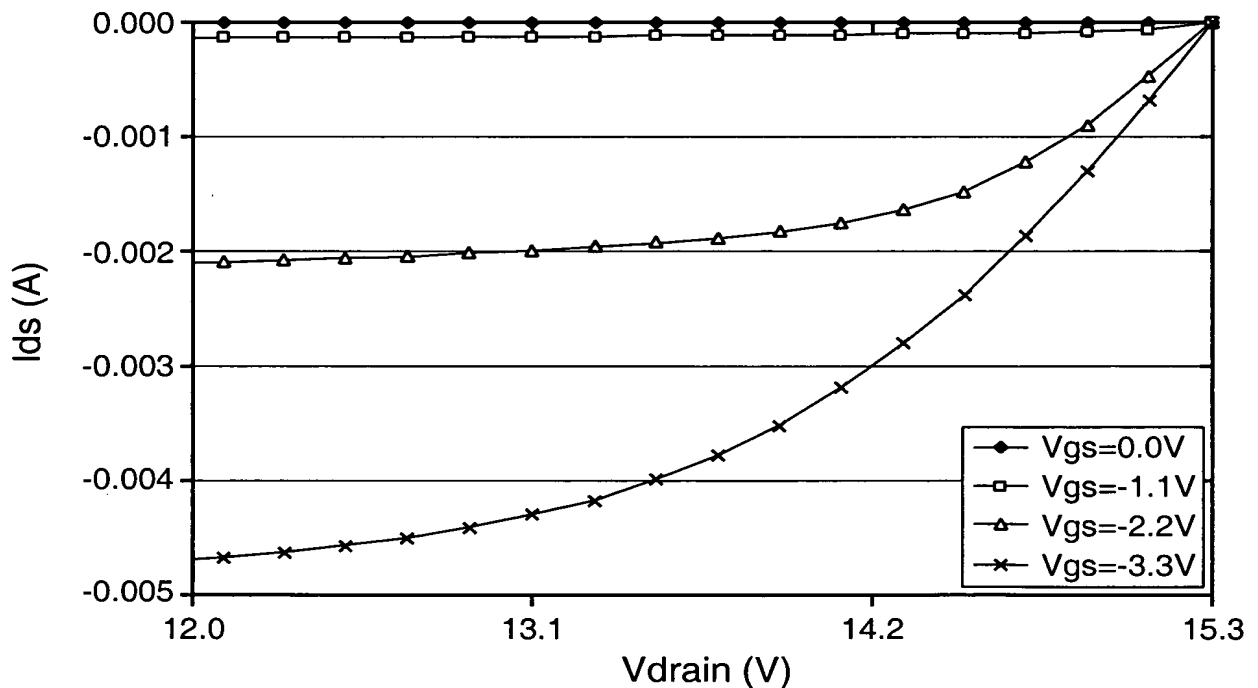


FIG._26B

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FIG._27

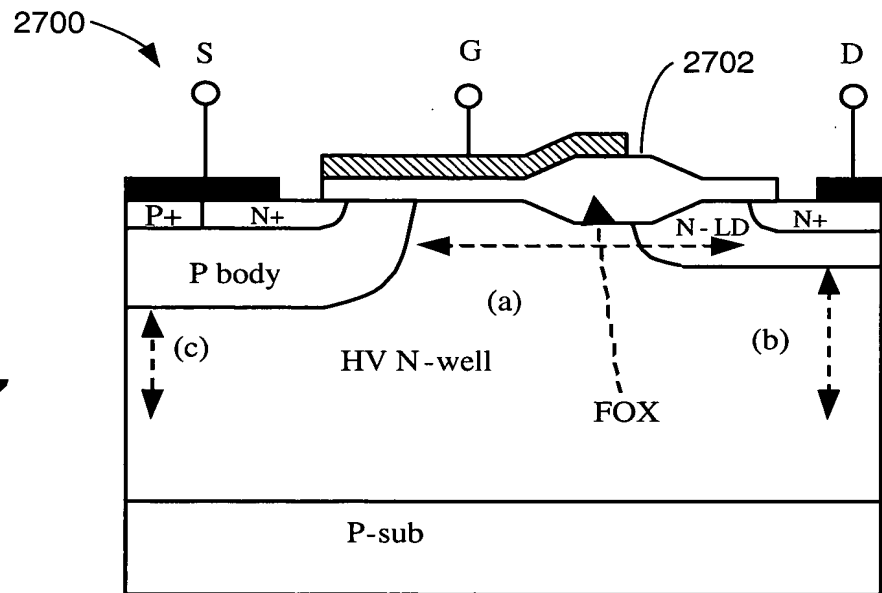


FIG._28

